

## COLLECTION OF CTL DECISIONS

<b>Standard: IEC 335-1, Ed.2</b>	<b>Sub clause: 30.3</b>	<b>Sheet n. 253 A Page 1(1)</b>
<b>Subject: Resistance to tracking</b>	<b>Key words: - tracking</b>	<b>Decision 16 of 34th meeting/1997</b>
<p><b>Question:</b> What is the minimum potential below which a tracking path is not any more liable to occur and therefore the test is not necessary ?</p> <p><b>Decision:</b> Based on subclauses 2.9.4, note 1, and 8.1.4, no tracking test is needed for SELV and PELV circuits.</p> <p><b>Explanatory notes: ---</b></p>		